

4-2 PLL ADJUSTMENT

ADJUSTMENT	ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
		UNIT	LOCATION		UNIT	ADJUST
REFERENCE FREQUENCY	<ul style="list-style-type: none"> • Displayed freq. : Any • L36 (PLL unit) : Center • Receiving 	PLL	Connect an RF voltmeter to check point P3. Connect a frequency counter to check point P3.	Maximum level (0 dBm or more) 60.000000 MHz	PLL	L37, L38
						L34, L36
REFERENCE LOOP LOCK VOLTAGE	<ul style="list-style-type: none"> • Displayed freq. : 0.03000 MHz • Mode : USB • Receiving 	PLL	Connect a digital multimeter or oscilloscope to check point CP1.	2.0 V	PLL	C104
MAIN LOOP LOCK VOLTAGE	<ul style="list-style-type: none"> • Displayed freq. : 30.00000 MHz • Mode : USB • Receiving 	PLL	Connect a digital multimeter or oscilloscope to check point CP2.	1.1 V	PLL	L3
	<ul style="list-style-type: none"> • Displayed freq. : 60.00000 MHz • Receiving 			1.1 V		L6

Ref-OSC Buff-out

60MHzの調整 (R60でも可能)

4-3 RECEIVER ADJUSTMENT

ADJUSTMENT	ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
		UNIT	LOCATION		UNIT	ADJUST
SENSITIVITY	<ul style="list-style-type: none"> • Displayed freq. : 14.10000 MHz • Mode : USB • [RIT] : OFF • [M4 AGC] : Fast (F AGC) • [M3 NB] : OFF • [P.AMP/ATT] : Preamp ON • [VOX GAIN] : Center • [ANTI VOX] : Max counterclockwise • [COMP GAIN] : Center • [BEEP/SIDE T] : Center • Connect a standard signal generator to [ANT 1] and set as: Frequency : 14.10150 MHz Level : 0.5 μV* (-113 dBm) • Receiving 	Rear panel	Connect an AC millivoltmeter to [EXT SP] jack with an 8 Ω dummy load.	Maximum AF output level	MAIN	Adjust repeatedly L47, L48, L55, L84, L85
				0.5 dB decrease from step 1.		L53
				0.5 dB decrease from step 2.		L54
				Maximum AF output level		PA
RECEIVER TOTAL GAIN	<ul style="list-style-type: none"> • Displayed freq. : 14.1000 MHz • Mode : USB • [P.AMP/ATT] : Preamp OFF • Connect a standard signal generator to [ANT 1] and set as: Frequency : 14.1015 MHz Level : 0.5 mV* (-53 dBm) and OFF • Modulation : OFF • Receiving 	Rear panel	Connect an AC millivoltmeter to [EXT SP] jack with an 8 Ω dummy load.	30 dB of AF level difference	MAIN	R401

*This output level of a standard signal generator (SSG) is indicated as SSG's open circuit.

• PLL AND PA UNITS

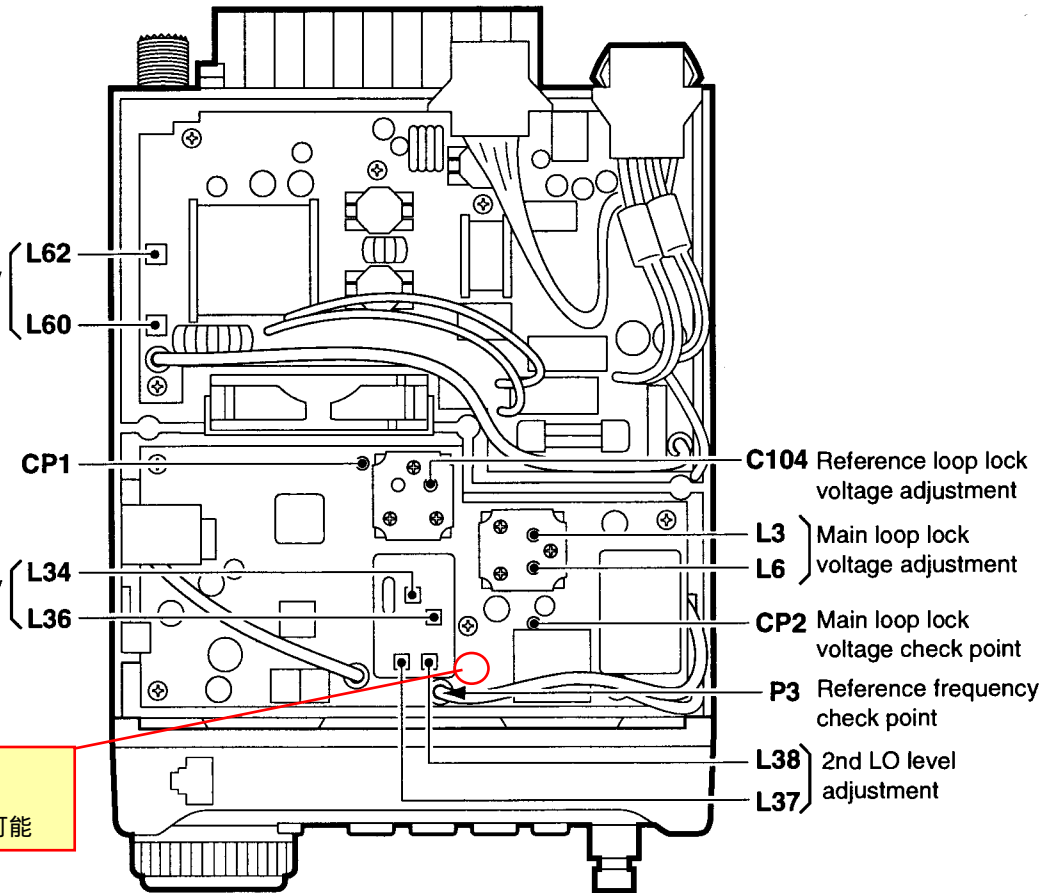
Sensitivity adjustment の 誤り
(VHF側)

~~Reference frequency
adjustment~~

Reference loop lock
voltage check point

Reference frequency
adjustment

この辺りにR60あり
CR-502取付に伴う改造前なら
R60でもRef-OSCの周波数調整可能



• MAIN UNIT

HF側のSensitivity adjustment

Sensitivity adjustment

R104 Receiver total gain
adjustment

L85 } Sensitivity adjustment
L84 }

