

## 4-2 PLL ADJUSTMENT

ADJUSTMENT	ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
		UNIT	LOCATION		UNIT	ADJUST
REFERENCE FREQUENCY	<ul style="list-style-type: none"> <li>Displayed freq. : Any</li> <li>L36 (PLL unit) : Center</li> <li>Receiving</li> </ul>	PLL	Connect an RF voltmeter to check point P3.  Connect a frequency counter to check point P3.	Maximum level (0 dBm or more)  60.000000 MHz	PLL	L37, L38 Ref-OSC Buff-out
						L34, L36 60MHzの調整 (R60でも可能)
REFERENCE LOOP LOCK VOLTAGE	<ul style="list-style-type: none"> <li>Displayed freq. : 0.03000 MHz</li> <li>Mode : USB</li> <li>Receiving</li> </ul>	PLL	Connect a digital multimeter or oscilloscope to check point CP1.	2.0 V	PLL	C104
MAIN LOOP LOCK VOLTAGE	<ul style="list-style-type: none"> <li>Displayed freq. : 30.00000 MHz</li> <li>Mode : USB</li> <li>Receiving</li> </ul>	PLL	Connect a digital multimeter or oscilloscope to check point CP2.	1.1 V	PLL	L3
	<ul style="list-style-type: none"> <li>Displayed freq. : 60.00000 MHz</li> <li>Receiving</li> </ul>			1.1 V		L6

## 4-3 RECEIVER ADJUSTMENT

ADJUSTMENT	ADJUSTMENT CONDITIONS	MEASUREMENT		VALUE	ADJUSTMENT	
		UNIT	LOCATION		UNIT	ADJUST
SENSITIVITY	<ul style="list-style-type: none"> <li>Displayed freq. : 14.10000 MHz</li> <li>Mode : USB</li> <li>[RIT] : OFF</li> <li>[M4 AGC] : Fast (F AGC)</li> <li>[M3 NB] : OFF</li> <li>[P.AMP/ATT] : Preamp ON</li> <li>[VOX GAIN] : Center</li> <li>[ANTI VOX] : Max counterclockwise</li> <li>[COMP GAIN] : Center</li> <li>[BEEP/SIDE T] : Center</li> <li>Connect a standard signal generator to [ANT 1] and set as:                Frequency : 14.10150 MHz                Level : 0.5 <math>\mu</math>V* (-113 dBm)</li> <li>Modulation : OFF</li> <li>Receiving</li> </ul>	Rear panel	Connect an AC millivoltmeter to [EXT SP] jack with an 8 $\Omega$ dummy load.	Maximum AF output level  0.5 dB decrease from step 1.  0.5 dB decrease from step 2.  Maximum AF output level	MAIN	Adjust repeatedly L47, L48, L55, L84, L85
						L53
						L54
						PA
RECEIVER TOTAL GAIN	<ul style="list-style-type: none"> <li>Displayed freq. : 14.1000 MHz</li> <li>Mode : USB</li> <li>[P.AMP/ATT] : Preamp OFF</li> <li>Connect a standard signal generator to [ANT 1] and set as:                Frequency : 14.1015 MHz                Level : 0.5 mV* (-53 dBm) and OFF</li> <li>Modulation : OFF</li> <li>Receiving</li> </ul>	Rear panel	Connect an AC millivoltmeter to [EXT SP] jack with an 8 $\Omega$ dummy load.	30 dB of AF level difference	MAIN	R401

\*This output level of a standard signal generator (SSG) is indicated as SSG's open circuit.

• PLL AND PA UNITS

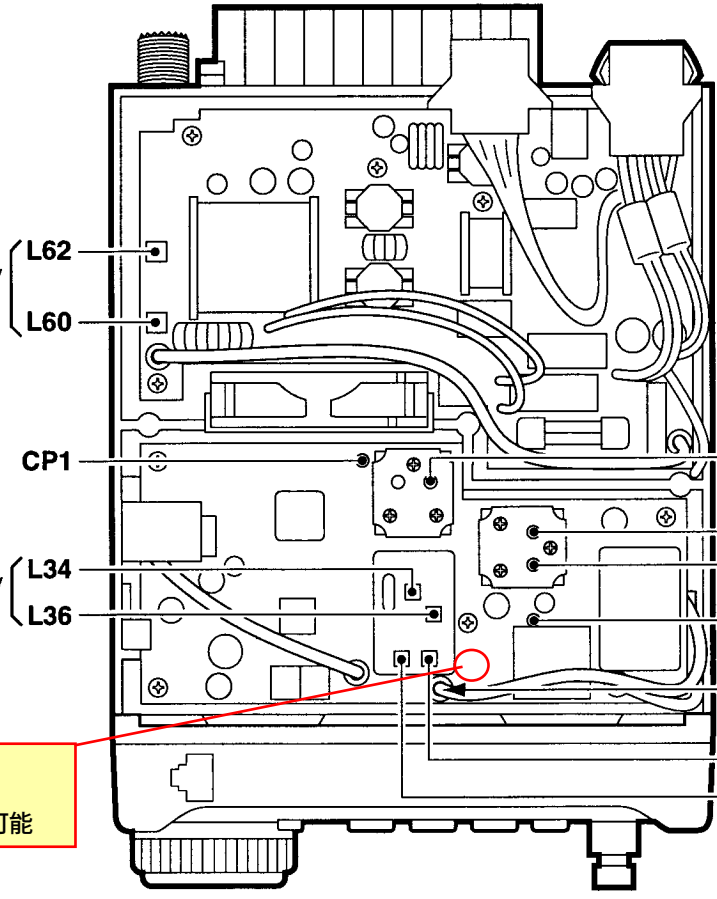
Sensitivity adjustment の 誤り  
(VHF側)

~~Reference frequency  
adjustment~~

Reference loop lock  
voltage check point

Reference frequency  
adjustment

この辺りにR60あり  
CR-502取付に伴う改造前なら  
R60でもRef-OSCの周波数調整可能



C104 Reference loop lock  
voltage adjustment

L3 } Main loop lock  
L6 } voltage adjustment

CP2 Main loop lock  
voltage check point

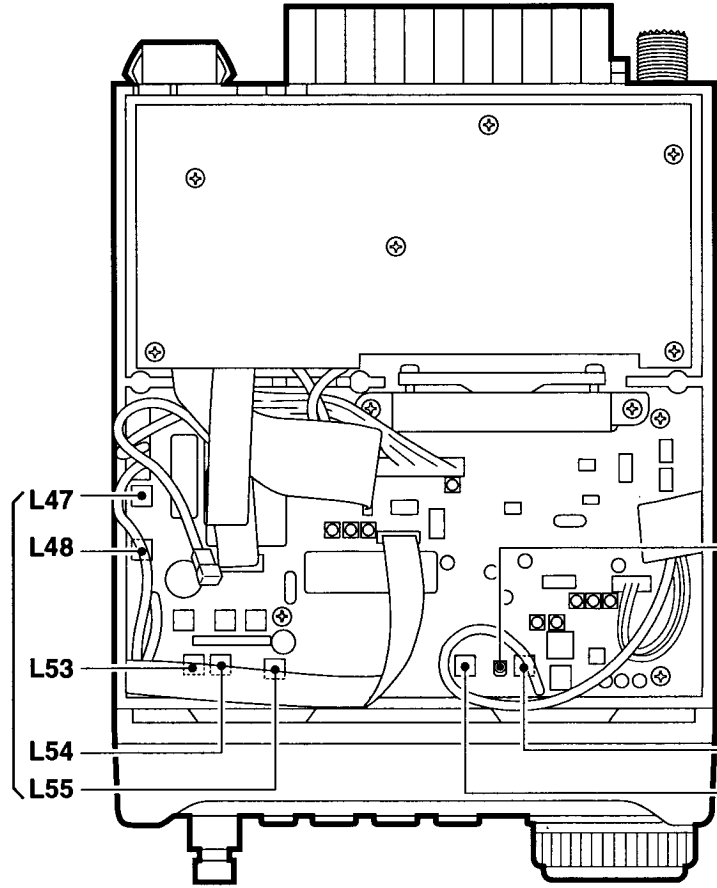
P3 Reference frequency  
check point

L38 } 2nd LO level  
L37 } adjustment

• MAIN UNIT

HF側のSensitivity adjustment

Sensitivity adjustment



R104 Receiver total gain  
adjustment

L85 } Sensitivity adjustment  
L84 }